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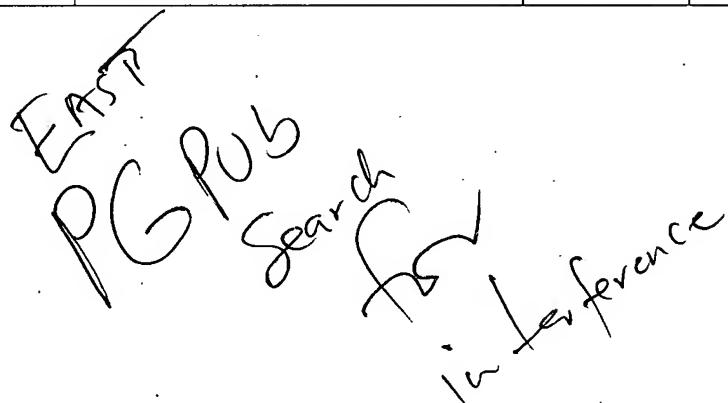| Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	570	((((detect$4 or check$3) near5 (error$1 or fault$1 or fail$4 or defect or anomalities))) near15 (CAM or "content addressable memory")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/22 14:04
L2	11	(((((detect$4 or check$3) near5 (error$1 or fault$1 or fail$4 or defect or anomalities))) near15 (CAM or "content addressable memory")) and parity and ("protection word" or CRC or "cyclic redundancy check")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/22 14:07
L3	7	(((((detect$4 or check$3) near5 (error$1 or fault$1 or fail$4 or defect or anomalities))) near15 (CAM or "content addressable memory")) and parity and ("protection word" or CRC or "cyclic redundancy check") and key	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/22 14:07
L4	6	(((((detect$4 or check$3) near5 (error$1 or fault$1 or fail$4 or defect or anomalities))) near15 (CAM or "content addressable memory")) and parity and ("protection word" or CRC or "cyclic redundancy check") and comparand	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/22 14:08
L5	6	(((((detect$4 or check$3) near5 (error$1 or fault$1 or fail$4 or defect or anomalities))) same (CAM or "content addressable memory")) and parity and ("protection word" or CRC or "cyclic redundancy check") and comparand	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/22 14:08
L6	6	(((((detect$4 or check$3) near5 (error$1 or fault$1 or fail$4 or defect or anomalities))) and (CAM or "content addressable memory")) and parity and ("protection word" or CRC or "cyclic redundancy check") and comparand	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/22 14:09

L7	6	(((((detect$4 or check$3) near10 (error$1 or fault$1 or fail$4 or defect or anomalities))) and (CAM or "content addressable memory")) and parity and ("protection word" or CRC or "cyclic redundancy check") and comparand	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/22 14:09
L8	285	(((((detect$4 or check$3) near10 (error$1 or fault$1 or fail$4 or defect or anomalities))) and (CAM or "content addressable memory")) and parity and ("protection word" or CRC or "cyclic redundancy check")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/22 14:10
L9	22	(((((detect$4 or check$3) near10 (error$1 or fault$1 or fail$4 or defect or anomalities))) and (CAM or "content addressable memory")) and ((parity same ("protection word" or CRC or "cyclic redundancy check")) same (compar$5 or comput$5))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/22 14:16
L10	5	(((((detect$4 or check$3) near10 (error$1 or fault$1 or fail$4 or defect or anomalities))) and (CAM or "content addressable memory")) and ((parity same ("protection word" or CRC or "cyclic redundancy check")) same (compar$5 or comput$5)) and key	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/22 15:00
L12	7	(((((detect$4 or check$3) near10 (error$1 or fault$1 or fail$4 or defect or anomalities))) same (CAM or "content addressable memory") near3 module$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/22 15:07
L13	9	((CAM or "content addressable memory") near3 module$1) and (parity near3 generat$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/22 15:04

L14	4	driediger-steven.in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/22 15:08

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L1	1	(((CAM or (content adj addressable adj memory)) near3 module$1) and "key-based parity" and "key-based protection" and comparand).clm.	US-PGPUB	OR	OFF	2007/06/22 15:16
L2	1	(((CAM or (content adj addressable adj memory)) near3 module$1) and "key-based parity" and "key-based protection" and compar$4).clm.	US-PGPUB	OR	OFF	2007/06/22 15:16

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Hung, L.D.; Goshima, M.; Sakai, S.;
<u>Computer Design: VLSI in Computers and Processors, 2005. ICCD 2005. Proceedings.</u>
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match detect circuits and bank selection scheme**
Kasai, G.; Takarabe, Y.; Furumi, K.; Yoneda, M.;
<u>Custom Integrated Circuits Conference, 2003. Proceedings of the IEEE 2003</u>
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Field-Programmable Custom Computing Machines, 2004. FCCM 2004. 12th Annual IEE
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☐   **6. One-transistor-cell multiple-valued CAM for a collision detection VLSI processor**
Hanyu, T.; Kanagawa, N.; Kameyama, M.;
Solid-State Circuits Conference, 1996. Digest of Technical Papers. 43rd ISSCC., 1996 II
International
8-10 Feb. 1996 Page(s):264 - 265, 457
Digital Object Identifier 10.1109/ISSCC.1996.488616

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☐   **7. FPGA based network intrusion detection using content addressable memories**
Long Bu; Chandy, J.A.;
Field-Programmable Custom Computing Machines, 2004. FCCM 2004. 12th Annual IEE
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Digital Object Identifier 10.1109/FCCM.2004.35

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Zhao, J.; Irrinki, S.; Puri, M.; Lombardi, F.;
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Pei Chi Ng; De Silva, L.C.;
Image Processing, 2001. Proceedings. 2001 International Conference on
Volume 3, 7-10 Oct. 2001 Page(s):266 - 269 vol.3
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Odagiri, H.; Takahashi, N.; Shidei, T.; Takeshita, K.; Kumagai, Y.;
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Hariyama, M.; Hanyu, T.; Kameyama, M.;
Intelligent Vehicles '94 Symposium, Proceedings of the
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Xin He; Frey, E.C.; Links, J.M.; Song, X.; Tsui, B.M.W.;
Nuclear Science, IEEE Transactions on
Volume 52, Issue 5, Part 1, Oct. 2005 Page(s):1359 - 1364
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